

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Jourdan et al.) Examiner: N/A
Application No.: 10/676,310) Art Unit: 2154
Filed: September 30, 2003)
For: METHOD AND SYSTEM FOR MULTIPLE)
BRANCH PATHS IN A MICROPROCESSOR)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR STATUS OF APPLICATION

Dear Commissioner:


The last correspondence received from the U.S. Patent and Trademark Office (PTO) was a "Notice of Publication mailed March 31, 2005". Please inform us on the current status of the above-referenced patent application.

If there are any further charges, please charge them to Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8/11/05


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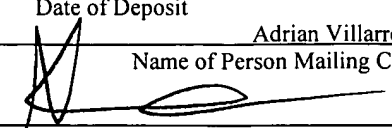
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